



SANYO Semiconductors

# DATA SHEET

## STK433-730-E — Thick-Film Hybrid IC 2-channel class AB audio power IC, 30W+30W

### Overview

The STK433-730-E is a hybrid IC designed to be used in 30W × 30W (2-channel) class AB audio power amplifiers.

### Applications

- Audio power amplifiers.

### Features

- Miniature package (47.0mm × 25.6mm × 9.0mm)
- Output load impedance:  $R_L = 6\Omega$  to  $4\Omega$  supported
- Built-in stand-by circuit, output limiting circuit for substrate overheating, and load short-circuit protection circuit constituted by monolithic ICs

### Series Models

	STK433-730-E	STK433-760-E
Output 1 (10%/1kHz)	30W×2 channels	50W×2 channels
Output 2 (0.4%/20Hz to 20kHz)	15W×2 channels	35W×2 channels
Max. rated $V_{CC}$ (quiescent)	±30V	±50V
Max. rated $V_{CC}$ (6 $\Omega$ )	±28V	±40V
Max. rated $V_{CC}$ (4 $\Omega$ )	±25V	±33V
Recommended operating $V_{CC}$ (4 $\Omega$ )	±18V	±23V
Dimensions (excluding pin height)	47.0mm×25.6mm×9.0mm	

■ Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.

■ Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

# STK433-730-E

## Specifications

**Absolute Maximum Ratings** at Ta = 25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max (0)	Stand-by ON or When no signal (Stand-by OFF)	±30	V
	V <sub>CC</sub> max (1)	When signals are present, R <sub>L</sub> ≥6Ω (*1)	±28	V
	V <sub>CC</sub> max (2)	When signals are present, R <sub>L</sub> ≥4Ω (*1)	±25	V
Minimum operating supply voltage	V <sub>CC</sub> min		±10	V
Stand-by pin maximum voltage	V <sub>ST</sub> max		-0.3 to +5.5	V
Output current	I <sub>O</sub> (peak)	1ch, ton=25ms	4.0	A
Thermal resistance	θj-c	Per power transistor	4.2	°C/W
		Per package	1.1	
Junction temperature	Tj max	Both the Tj max and Tc max conditions must be met.	150	°C
IC substrate operating temperature	Tc max		125	°C
Storage temperature	Tstg		-30 to +125	°C

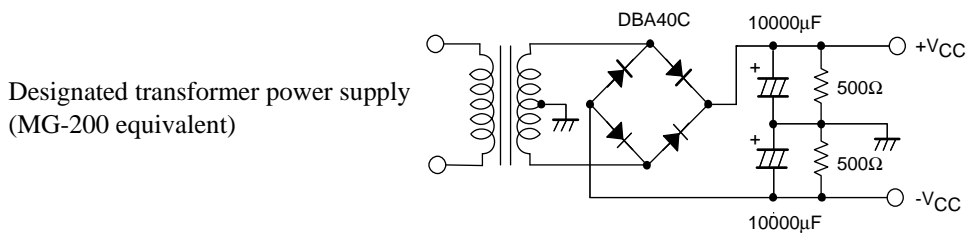
**Operating Characteristics** at Tc=25°C, R<sub>L</sub>=4Ω, Rg=600Ω, VG=30dB, non-inductive load R<sub>L</sub>, using constant-voltage power supply and specification test circuit, unless otherwise specified

Parameter	Symbol	Conditions *2				Ratings			unit	
		V <sub>CC</sub> (V)	f (Hz)	P <sub>O</sub> (W)	THD (%)	min	typ	max		
Output power *2	P <sub>O</sub> (1)	±18	20 to 20k		0.4	10	15		W	
	P <sub>O</sub> (2)	±18	1k		0.4		20			
	P <sub>O</sub> (3)	±18.5	1k		10		30			
Total harmonic distortion *2	THD (1)	±18	20 to 20k	5.0	VG=30dB			0.4	%	
	THD (2)	±18	1k				0.08			
Output power transistor saturation voltage	V <sub>sat</sub>	±18	1k	30	10		3.8		V	
Frequency characteristics *2	f <sub>L</sub> , f <sub>H</sub>	±18		1.0		+0 -3dB	20 to 50k		Hz	
Input impedance	r <sub>i</sub>	±18	1k	1.0			55		kΩ	
Output noise voltage *10	V <sub>NO</sub>	±21.5				Rg=2.2kΩ		1.0	mVrms	
Quiescent current	I <sub>CCO</sub>	±21.5				No loading	12	25	50	mA
Output neutral voltage	V <sub>N</sub>	±21.5					-70	0	+70	mV
Pin 13 voltage when standby ON *5	V <sub>ST</sub> ON	±18				Standby		0	0.6	V
Pin 13 voltage when standby OFF *5	V <sub>ST</sub> OFF	±18				Operating	2.5	3.6	5.5	V
Pin 10 (latch operation detection pin) voltage *7	IM ON					In short-circuit protection mode		6.2		V
Substrate thermal protection *8	TD	±18	1k					125		°C
Overcurrent protection *8,*10	I <sub>O</sub> (peak)	±18	1k					4.0		A

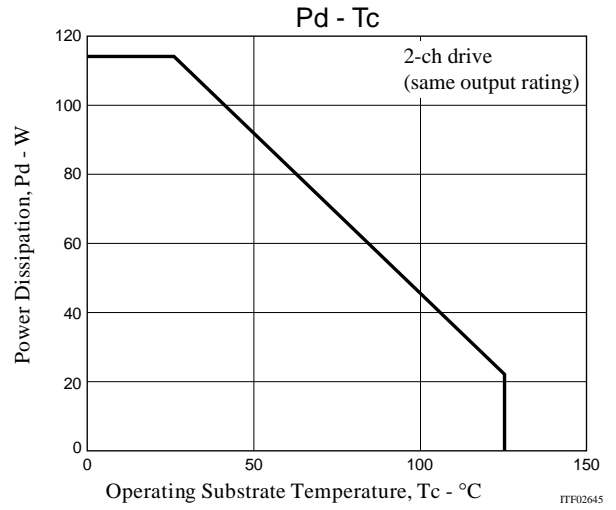
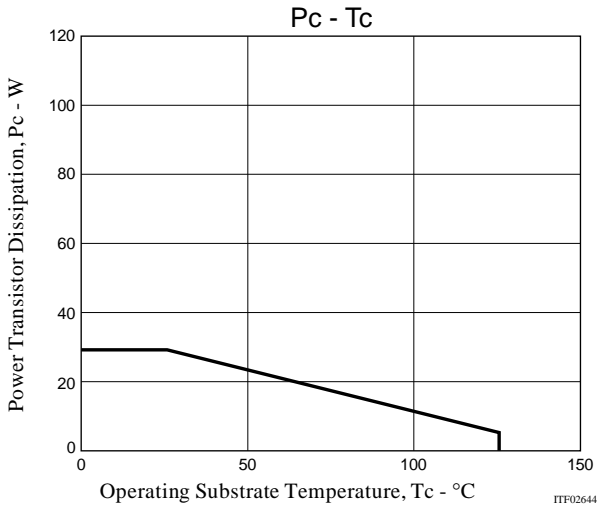
## STK433-730-E

### [Remarks]

- \*1: Maximum ratings are limits beyond which damage to the device may occur.  
Exceeding the maximum ratings, even momentarily, may cause damage to the hybrid IC.  
In SANYO Semiconductor's test processes, operation at the maximum supply voltage is checked.  
(Test conditions)  $V_{CC}$  max (2)= $\pm 25V$ ,  $R_L=4\Omega$ ,  $f=1kHz$ ,  $P_o=15W$ , 1ch Drive,  $t_{on}=25ms$ ,  $T_c=25^\circ C$
- \*2: For 1-channel operation
- \*3: -Pre  $V_{CC}$  (pin 1) must be connected to the lowest stable potential to prevent the current flowing into the pin 1 due to reverse bias, etc.
- \*4: Thermal design must be implemented based on the conditions under which the customer's end products are expected to operate on the market.
- \*5: Use the hybrid IC so that the voltage applied to the stand-by pin (pin 13) never exceeds the maximum rating. The power amplifier is turned on by applying +2.5V to +5.5V to the stand-by pin (pin 13).
- \*6: An output limiting circuit for substrate overheating is incorporated to protect the hybrid IC from the heat generation exceeding the maximum rating. Thermal design must be implemented from the maximum loss  $P_d$  max and "Pd-Tc" derating curve based on the conditions under which the customer's end products are expected to operate on the market. When deviating from the "Pd-Tc" derating curve, the desired output is not obtained, but the prescribed output is generated again by reducing the substrate temperature to within the recommended operating region.
- \*7: The load short-circuit protection is designed based on the specification test condition.  
The load short-circuit protection circuit is activated when it has detected an overcurrent in the output transistors. So if any deviation from the "Pd-Tc" derating curve occurs, the protection circuit is activated and the circuit shuts down in order to protect the output transistors. When the load short-circuit protection circuit has been activated and the circuit shuts down, approximately +6.2V of voltage will be placed at the MONITOR pin (pin 10) (normally 0V). The protection circuit operation is released by establishing the stand-by mode (pin 13: 0V).
- \*8: The substrate temperature protection rating is the design guarantee value using the specification test circuit of SANYO Semiconductor.  
The output limiting circuit for substrate overheating (\*6) and the load short-circuit protection circuit (\*7) are the only protection functions incorporated.  
The thermal design and overcurrent protection level must be verified based on the conditions under which the customer's end products are expected to operate on the market.
- \*9: A thermoplastic adhesive resin is used to secure the case and aluminum substrate. For this reason, the hybrid IC must be fixed to the heat sink before soldering and mounted. The heat sink must be installed or removed at room temperature.
- \*10: Use the designated transformer power supply circuit shown in the figure below for the measurement of allowable load shorted time and output noise voltage level.
- \*11: Weight of independent hybrid IC: 12.2g  
Outer box dimensions: 452(D)  $\times$  325(W)  $\times$  192(H) mm

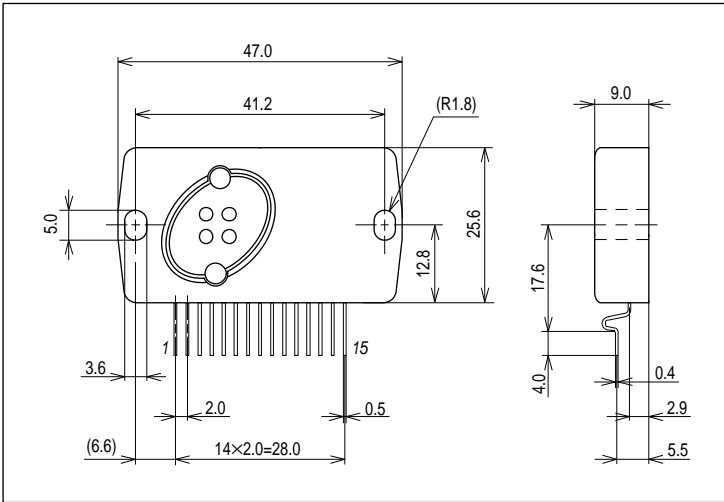


# STK433-730-E



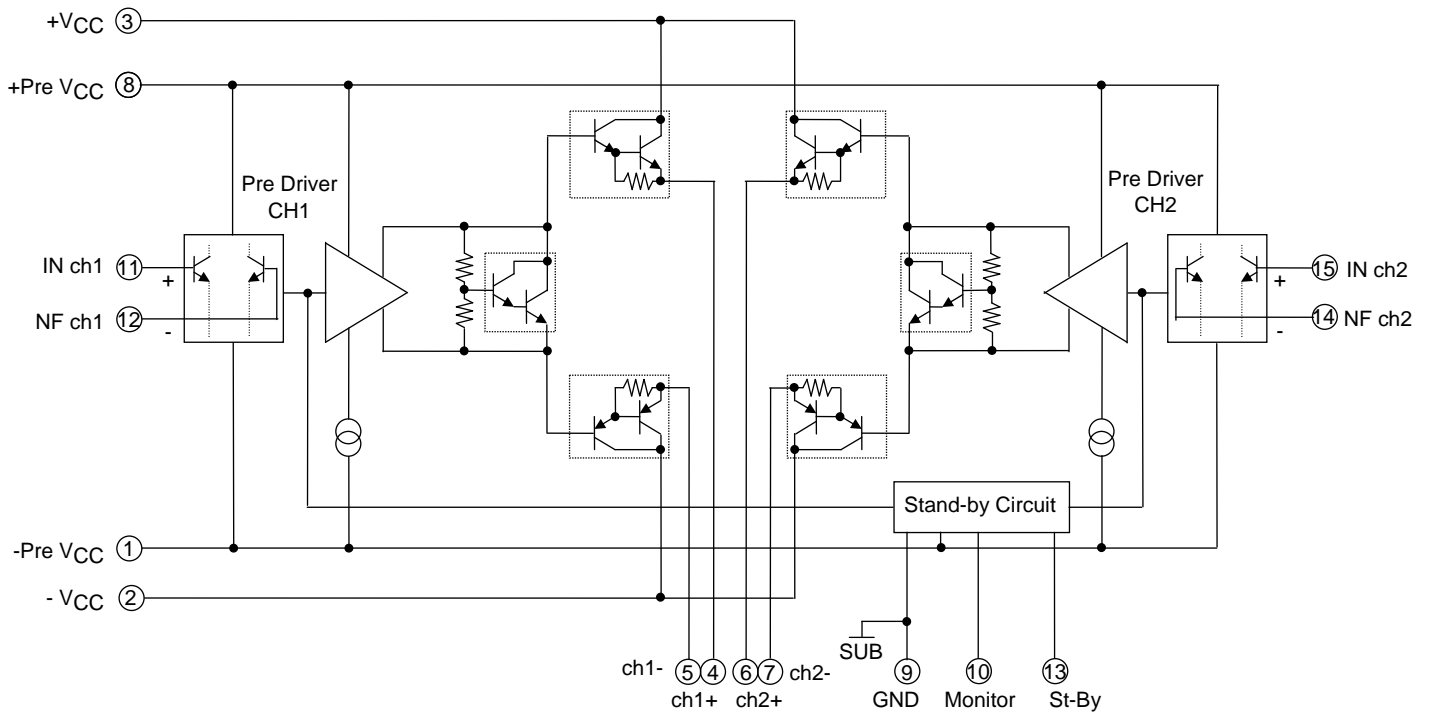
## Package Dimensions

unit:mm (typ)

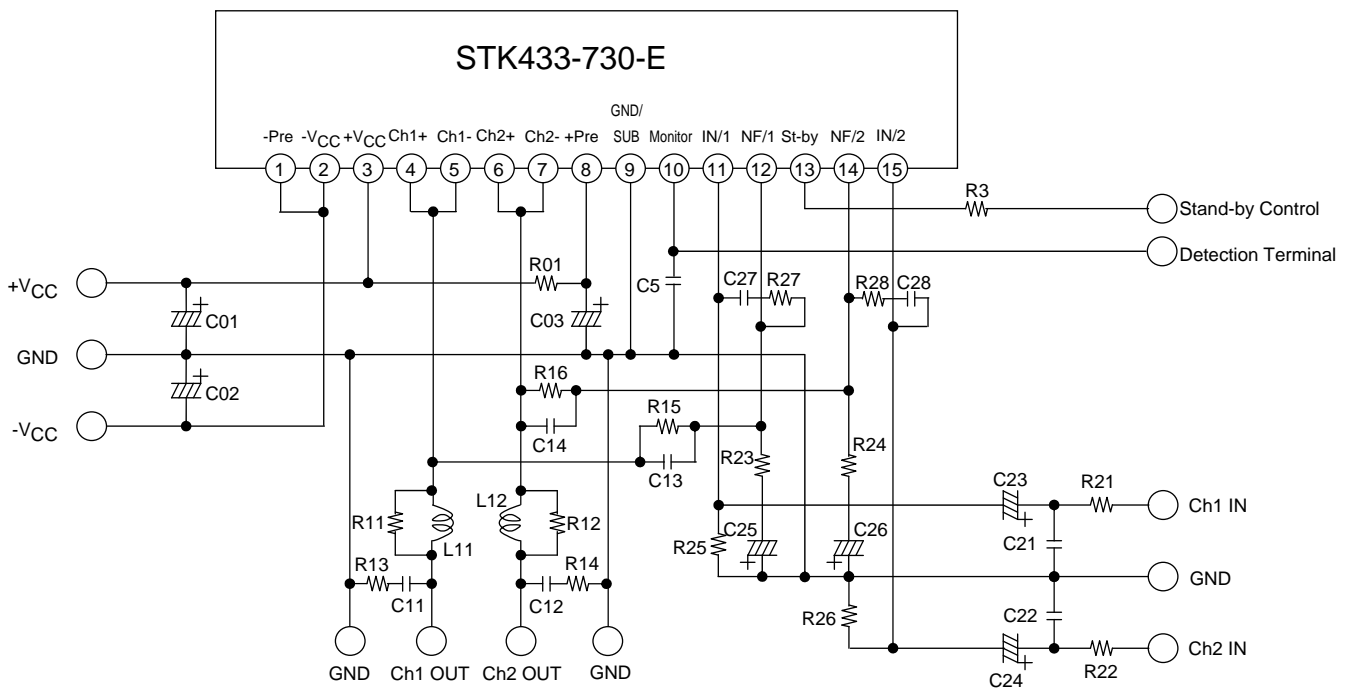


# STK433-730-E

## Internal Equivalent Circuit



## Application Circuit Example



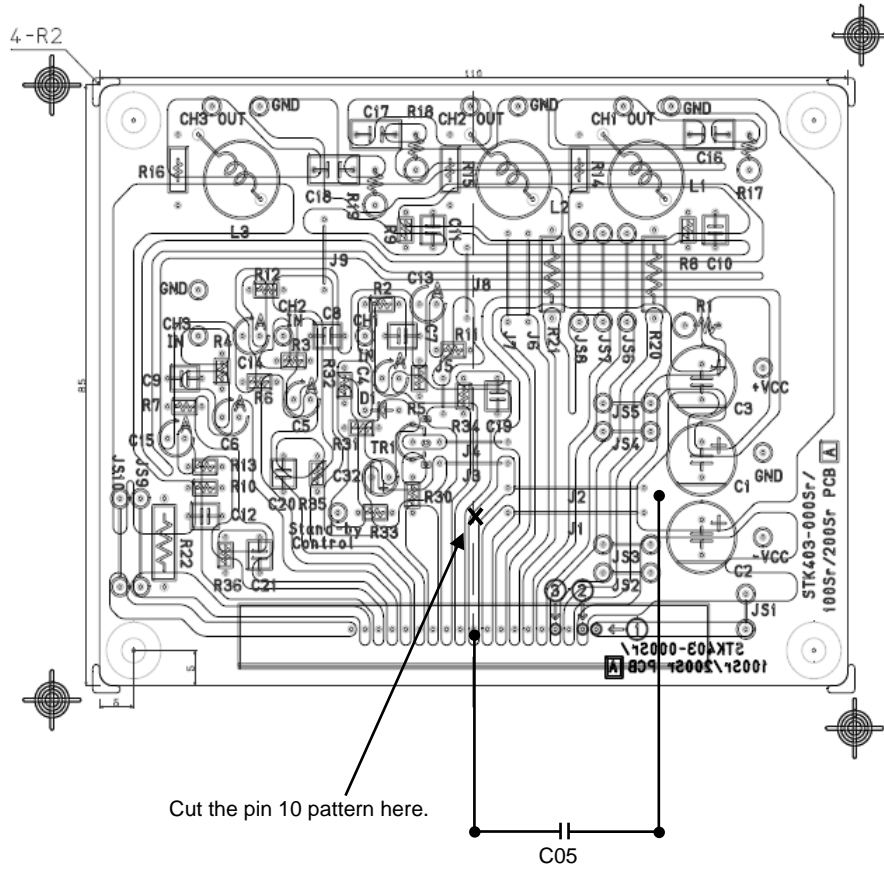
## STK433-730-E

### Recommended Values for Application Parts (for the test circuit)

Symbol	Recommended Value	Description	Larger than Recommended Value	Smaller than Recommended Value
R01	47Ω	Ripple filtering resistors (Fusible resistors are desirable) (Used with C03 to form a ripple filter.)	Decreased pass-through current at high frequencies.	Increased pass-through current at high frequencies.
R03	-	Use a limiting resistor according to the stand-by control voltage in order to control the stand-by pin voltage VST within the rating.		
R11, 12	4.7Ω	Noise-absorbing resistors	-	-
R13, 14	4.7Ω/1W	Oscillation prevention	-	-
R15, 16	56kΩ	Used with R23 and R24 to determine the voltage gain VG.	VN offset (Ensure R15=R25, R16=R26 when changing.)	
R21, 22	1kΩ	Input filtering resistor	-	-
R23, 24	1.8kΩ	Used with R15 and R16 to determine the voltage gain VG. (VG should desirably be determined by the R23 and R24 value.)	Likely to oscillate (VG<30dB)	None (VG≤42dB)
R25, 26	56kΩ	Input bias resistors (Virtually determine the input impedance.)	-	-
R27, 28	3kΩ	Oscillation prevention	Likely to oscillate	
C01, 02	100μF	Oscillation prevention • Insert the capacitors as close to the IC as possible to decrease the power impedance for reliable IC operation (use of electrolytic capacitors are desirable).	-	-
C03	200μF	Decoupling capacitors. • Eliminate ripple components that pass into the input side from the power line. (Used with R01 to form a filter.)	Increase in ripple components that pass into the input side from the power line.	
C05	0.01μF	A detection voltage appears during the protection operation. Adjust the time constant (when the detection pin is to be used).		
C11, 12	0.1μF	Oscillation prevention (Mylar capacitors are recommended.)	Likely to oscillate	
C13, 14	12pF	Oscillation prevention	Likely to oscillate	
C21, 22	470pF	Input filter capacitor (Used with R21 and R22 to form a filter that suppresses high-frequency noises.)	-	-
C23, 24	2.2μF	Input coupling capacitor (block DC current)	-	-
C25, 26	10μF	NF capacitor (Changes the low cutoff frequency; $f_L=1/(2\pi \cdot C25 \cdot R23)$ )	Increase in low-frequency voltage gain, with higher pop noise at power-on.	Decrease in low-frequency voltage gain
C27, 28	2200pF	Oscillation prevention	Likely to oscillate	
L11, 12	1μH	Oscillation prevention	None	Likely to oscillate

# STK433-730-E

## Sample PCB Trace Pattern



\* Additional components are indicated by their circuit location numbers.

## STK433-730-E

### STK433-730-E TEST Board PARTS LIST

STK403-000sr/100sr/200sr PCB

PCB Location No.	CIRCUIT Location No.	PARTS	RATING	STK433-730-E
R01	R01	ERG1SJ101	47Ω, 1W	
R02, R03	R21, R22	RN16S102FK	1kΩ, 1/6W	
R05, R06, R08, R09	R15, R16, R25, R26	RN16S563FK	56kΩ, 1/6W	
R11, R12	R23, R24	RN16S182FK	1.8kΩ, 1/6W	
R14, R15	R11, R12	RN14S4R7FK	4.7Ω, 1/4W	
R17, R18	R13, R14	ERX1SJ4R7	4.7Ω, 1W	
R20, R21	-	-	-	short
R34, R35	R27, R28	RN16S561FK	3kΩ, 1/6W	
C01, C02	C01, C02	50MV100HC	100μF, 50V	
C03	C03	50MV200HC	200μF, 50V	
-	C05	ECQ-V1H103JZ	0.01μF, 50V	
C05, C06	C23, C24	50MV2R2HC	2.2μF, 50V	(*)
C07, C08	C21, C22	DD104-63B471K50	470pF, 50V	
C10, C11	C13, C14	DD104-63CJ120C50	12pF, 50V	
C13, C14	C25, C26	10MV10HC	10μF, 10V	(*)
C16, C17	C11, C12	ECQ-V1H104JZ	0.1μF, 50V	
C19, C20	C27, C28	DD104-63B222K50	2200pF, 50V	
L01, L02	L11, L12	-	1μH	
Stand-By Control Circuit	Tr1	2SC2274(Reference)	VCE≥50V, IC≥10mA	
	R3	RN16S133FK	1kΩ, 1/6W	
	R31	RN16S333FK	33kΩ, 1/6W	
	R33	RN16S202FK	2kΩ, 1/6W	
	C32	10MV33HC	33μF, 10V	
J1, J2, J3, J4, J5, J6, J8, J9	-	-	-	Jumper
JS6	-	-	-	Jumper
JS1	-	-	-	Jumper

- (\*) Capacitor mark "A" side is "-" (negative).
- C05 does not have a location number on the PCB so the component must be mounted on the reverse side of the board.



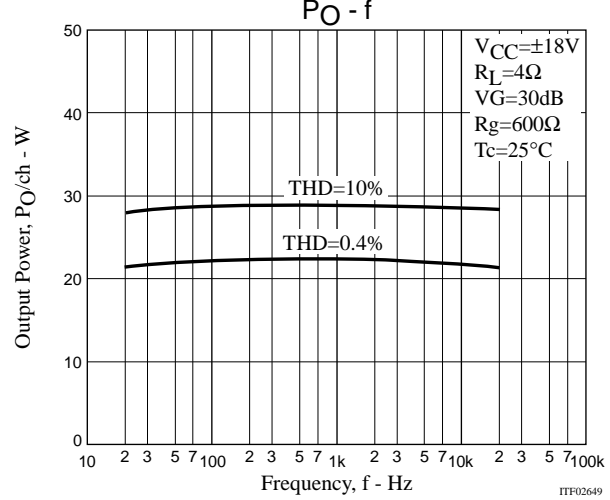
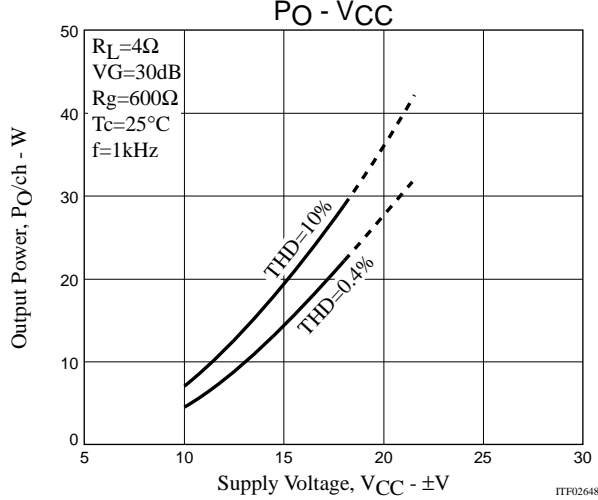
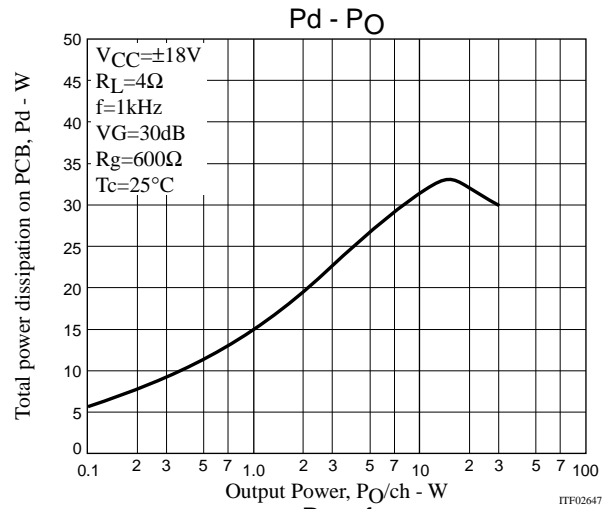
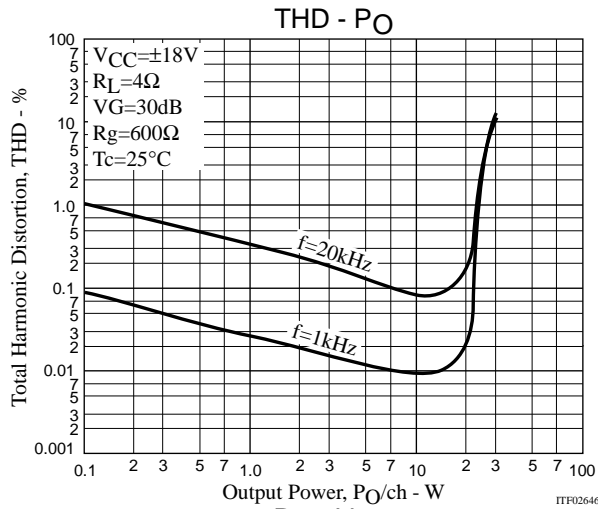
# STK433-730-E

## Pin Assignments

[STK433-730-E/-760-E Pin Layout]

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
(Size) 47.0mm×25.6mm×9.0mm	2ch classAB/2.00mm																		
STK433-730-E 30W×2ch/JEITA	-	-	+	O	O	O	O	+		M	I	N	S	N	I				
STK433-760-E 50W×2ch/JEITA	P	V	V	U	U	U	U	P	S	O	N	F	T	F	I				
	R	C	C	T	T	T	T	R	U	N	/	/	A	/	/				
	E	C	C	/	/	/	/	E	B	I	C	C	N	C	C				
				C	C	C	C		/	T	H	H	D	H	H				
				H	H	H	H		G	O	1	1		2	2				
				1	1	2	2		N	R			B						
				+	-	+	-		D				Y						

## Evaluation Board Characteristics



## STK433-730-E

[Thermal Design Example for STK433-730-E ( $R_L = 4\Omega$ )]

The thermal resistance,  $\theta_{c-a}$ , of the heat sink for total power dissipation,  $P_d$ , within the hybrid IC is determined as follows.

Condition 1: The hybrid IC substrate temperature,  $T_c$ , must not exceed  $125^\circ\text{C}$ .

$$P_d \times \theta_{c-a} + T_a < 125^\circ\text{C} \dots\dots\dots (1)$$

$T_a$ : Guaranteed ambient temperature for the end product

Condition 2: The junction temperature,  $T_j$ , of each power transistor must not exceed  $150^\circ\text{C}$ .

$$P_d \times \theta_{c-a} + P_d/N \times \theta_{j-c} + T_a < 150^\circ\text{C} \dots\dots\dots (2)$$

$N$ : Number of power transistors

$\theta_{j-c}$ : Thermal resistance per power transistor

However, the power dissipation,  $P_d$ , for the power transistors shall be allocated equally among the number of power transistors.

The following inequalities result from solving equations (1) and (2) for  $\theta_{c-a}$ .

$$\theta_{c-a} < (125 - T_a)/P_d \dots\dots\dots (1)'$$

$$\theta_{c-a} < (150 - T_a)/P_d - \theta_{j-c}/N \dots\dots\dots (2)'$$

Values that satisfy these two inequalities at the same time represent the required heat sink thermal resistance.

When the following specifications have been stipulated, the required heat sink thermal resistance can be determined from formulas (1)' and (2)' .

- |                                  |          |
|----------------------------------|----------|
| • Supply voltage                 | $V_{CC}$ |
| • Load resistance                | $R_L$    |
| • Guaranteed ambient temperature | $T_a$    |

[Example]

When the IC supply voltage,  $V_{CC}$ , is  $\pm 18\text{V}$  and  $R_L$  is  $4\Omega$ , the total power dissipation,  $P_d$ , within the hybrid IC, will be a maximum of  $33\text{W}$  at  $1\text{kHz}$  for a continuous sine wave signal according to the  $P_d$ - $P_O$  characteristics.

For the music signals normally handled by audio amplifiers, a value of  $1/8P_O \text{ max}$  is generally used for  $P_d$  as an estimate of the power dissipation based on the type of continuous signal. (Note that the factor used may differ depending on the safety standard used.)

This is:

$$P_d = 24.09\text{W} \quad (\text{when } 1/8P_O \text{ max.} = 3.75\text{W}).$$

The number of power transistors in audio amplifier block of these hybrid ICs,  $N$ , is 4, and the thermal resistance per transistor,  $\theta_{j-c}$ , is  $8.0^\circ\text{C/W}$ . Therefore, the required heat sink thermal resistance for a guaranteed ambient temperature,  $T_a$ , of  $50^\circ\text{C}$  will be as follows.

$$\begin{aligned} \text{From formula (1)'} \quad \theta_{c-a} &< (125 - 50)/24.09 \\ &< 3.11 \end{aligned}$$

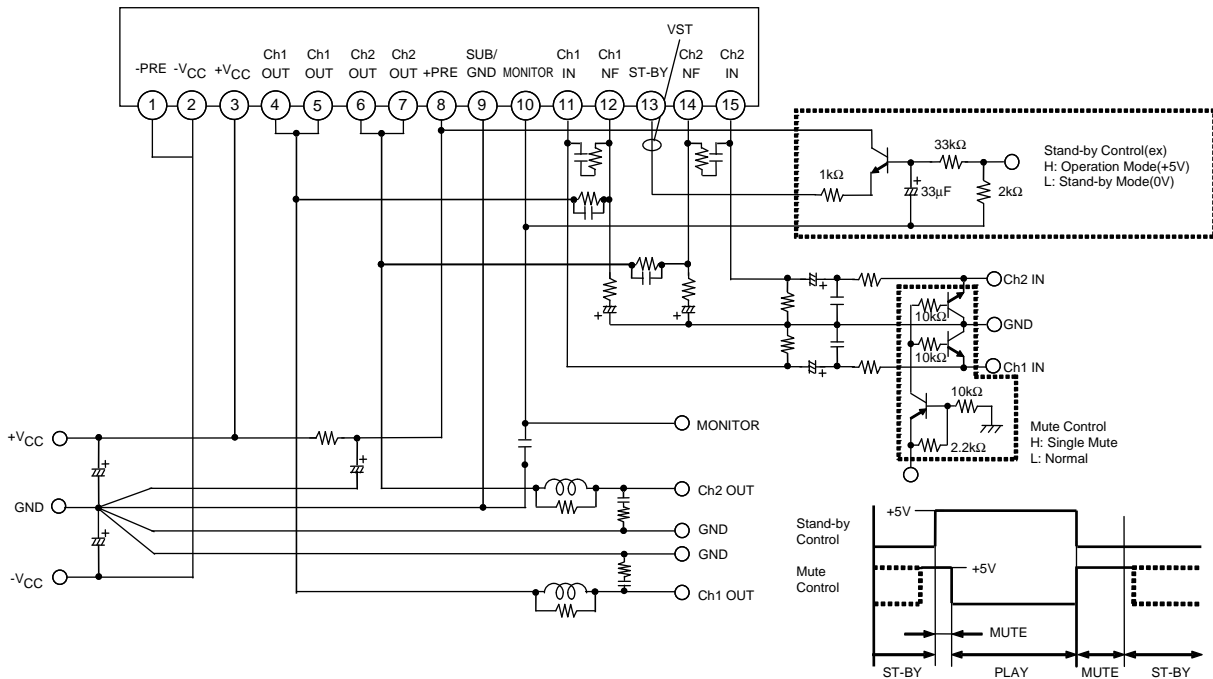
$$\begin{aligned} \text{From formula (2)'} \quad \theta_{c-a} &< (150 - 50)/24.09 - 4.2/4 \\ &< 3.10 \end{aligned}$$

Therefore, the value of  $3.10^\circ\text{C/W}$ , which satisfies both of these formulae, is the required thermal resistance of the heat sink.

Note that this thermal design example assumes the use of a constant-voltage power supply, and is therefore not a verified design for any particular user's end product.

STK433-700series Stand-by Control & Mute Control Application

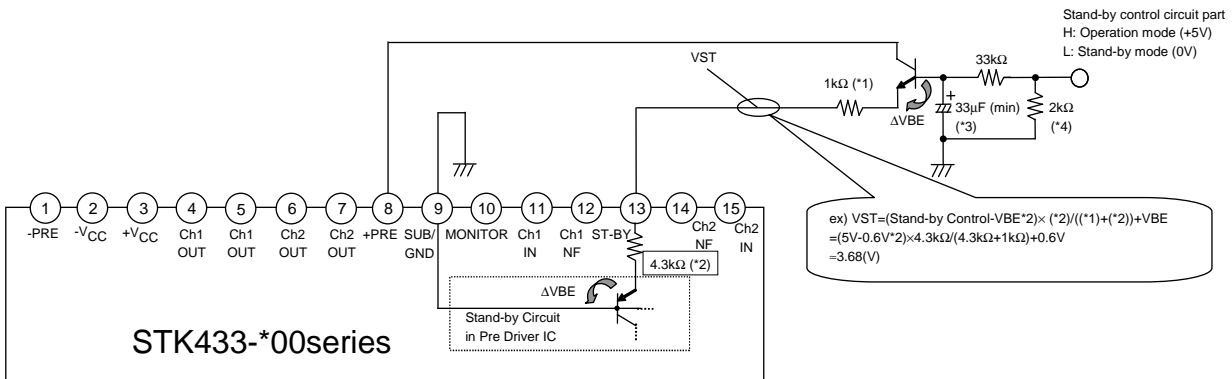
STK433-700series



[The example of use STK433-\*00series Stand-by control circuit]

Features

- By using the recommended stand-by control application, the pop noise level when the power is turned on/off can be significantly reduced.
  - By adjusting the limiting resistance (\*1) in accordance with the voltages of the microcontroller and other components used, it is possible to perform stand-by control, facilitating the finished product design effort.
- (ex) STK433-\*00series test circuit. When impressed by Stand-by control control [+5V].



Operation Explanation

1) About VST (#13pin Stand-by Threshold)

<1> Operation Mode

When pin 13 reference voltage VST is equal to or greater than 2.5 V, the stand-by circuit is set OFF, and the amplifier is set to the operation mode.

<2> Stand-by Mode

When pin 13 reference voltage VST is equal to or less than 0.6V, the stand-by circuit is set ON, and the amplifier is set to the stand-by mode.

- (\*3) The pop noise that occurs when the power is turned ON is reduced by providing a time constant using a capacitor during operation.
- (\*4) The pop noise level is reduced by discharging the capacitor with a resistor in the stand-by mode.

- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of May, 2009. Specifications and information herein are subject to change without notice.