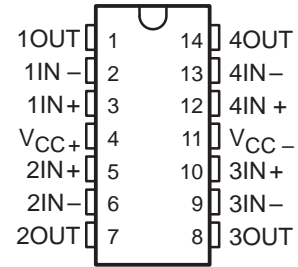


- Low Input Bias Current . . . 50 pA Typ
- Low Input Noise Current
0.01 pA/√Hz Typ
- Low Total Harmonic Distortion
- Low Supply Current . . . 8 mA Typ
- Gain Bandwidth . . . 3 MHz Typ
- High Slew Rate . . . 13 V/μs Typ
- Pin Compatible With the LM348

D OR N PACKAGE
(TOP VIEW)



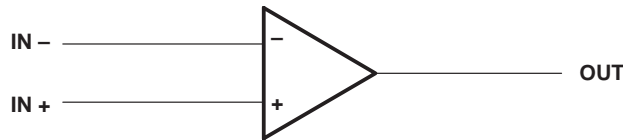
description

These devices are low-cost, high-speed, JFET-input operational amplifiers. They require low supply current yet maintain a large gain-bandwidth product and a fast slew rate. In addition, their matched high-voltage JFET inputs provide very low input bias and offset current.

The LF347 and LF347B can be used in applications such as high-speed integrators, digital-to-analog converters, sample-and-hold circuits, and many other circuits.

The LF347 and LF347B are characterized for operation from 0°C to 70°C.

symbol (each amplifier)



AVAILABLE OPTIONS

T _A	V _{IO} max AT 25°C	PACKAGE	
		SMALL OUTLINE (D)	PLASTIC DIP (N)
0°C to 70°C	10 mV	LF347D	LF347N
	5 mV	LF347BD	LF347BN

The D packages are available taped and reeled. Add R suffix to the device type (e.g., LF347DR).

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} +	18 V
Supply voltage, V _{CC} -	-18 V
Differential input voltage, V _{ID}	±30 V
Input voltage, V _I (see Note 1)	±15 V
Duration of output short circuit	unlimited
Continuous total power dissipation	See Dissipation Rating Table
Operating temperature range	0°C to 70°C
Storage temperature range	-65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

NOTE 1: Unless otherwise specified, the absolute maximum negative input voltage is equal to the negative power supply voltage.

LF347, LF347B
JFET-INPUT
QUAD OPERATIONAL AMPLIFIERS

SLOS013B – MARCH 1987 – REVISED AUGUST 1994

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING
D	608 mW	7.6 mW/°C	61°C	608 mW
N	680 mW	N/A	N/A	680 mW

recommended operating conditions

	MIN	MAX	UNIT
Supply voltage, V _{CC} +	3.5	18	V
Supply voltage, V _{CC} –	–3.5	–18	V

electrical characteristics over operating free-air temperature range, V_{CC±} = ±15 V (unless otherwise specified)

PARAMETER	TEST CONDITIONS	T _A †	LF347			LF347B			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO} Input offset voltage	V _{IC} = 0, R _S = 10 kΩ	25°C	5	10		3	5	mV	
		Full range			13		7		
α _{VIO} Average temperature coefficient of input offset voltage	V _{IC} = 0, R _S = 10 kΩ		18			18		μV/°C	
I _{IO} Input offset current ‡	V _{IC} = 0	25°C	25	100		25	100	pA	
		70°C		4			4	nA	
I _{IB} Input bias current ‡	V _{IC} = 0	25°C	50	200		50	200	pA	
		70°C		8			8	nA	
V _{ICR} Common-mode input voltage range			±11	–12 to 15		±11	–12 to 15	V	
V _{OM} Maximum peak output voltage swing	R _L = 10 kΩ		±12	±13.5		±12	±13.5	V	
A _{VD} Large-signal differential voltage	V _O = ±10 V, R _L = 2 kΩ	25°C	25	100		50	100	V/mV	
		Full range	15			25			
r _i Input resistance	T _A = 25°C		10 ¹²			10 ¹²		Ω	
CMRR Common-mode rejection ratio	R _S ≤ 2 kΩ		70	100		80	100	dB	
k _{SVR} Supply-voltage rejection ratio	See Note 2		70	100		80	100	dB	
I _{CC} Supply current			8	11		8	11	mA	

† Full range is 0°C to 70°C.

‡ Input bias currents of a FET-input operational amplifier are normal junction reverse currents, which are temperature sensitive. Pulse techniques must be used that will maintain the junction temperatures as close to the ambient temperature as possible.

NOTE 2: Supply-voltage rejection ratio is measured for both supply magnitudes increasing or decreasing simultaneously.

operating characteristics, V_{CC±} = ±15 V

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{O1} /V _{O2} Crosstalk attenuation	f = 1 kHz		120		dB
SR Slew rate		8	13		V/μs
B ₁ Unity-gain bandwidth			3		MHz
V _n Equivalent input noise voltage	f = 1 kHz, R _S = 20 Ω		18		nV/√Hz
I _n Equivalent input noise current	f = 1 kHz		0.01		pA/√Hz



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