



Sanken Power Devices from Allegro MicroSystems

STR-A6259H

Universal-Input 13 W 100 kHz Flyback Switching Regulators

Supproximate Scale 1:1

FEATURES AND BENEFITS

- 100 kHz PWM with ±5% frequency jittering for EMI noise filtering cost reduction
- Rugged 650 V avalanche-rated MOSFET:
 - Simplified surge absorption
 - No V_{DSS} derating required
- Low $R_{DS(on)}$: 6 Ω maximum
- Auto-burst mode for stand-by operation or light loads; less transformer audible noise
- Built-in leading edge blanking
- Soft start and low start-up current; start-up circuit disabled in operation
- Auto-burst stand-by (intermittent operation) input power <0.1 W at no load</p>
- Built-in constant-voltage/constant current (CV/CC)
- Multiple protections:
 - Pulse-by-pulse overcurrent protection (OCP)
 - Overload protection (OLP) with auto restart
 - Latching overvoltage protection (OVP)
 - Undervoltage lockout (UVLO) with hysteresis
 - Latching thermal shutdown (TSD)

The STR-A6259H is a 100 kHz PWM topology (with \pm 5% frequency jittering for minimum EMI) regulator specifically designed to satisfy the requirements for increased integration and reliability in flyback converters. It incorporates a primary control and drive circuit with an avalanche-rated power MOSFET.

Covering the power range from below 17 watts for a 230 VAC input, or to 13 watts for a universal (85 to 264 VAC) input, this device can be used in a wide range of applications, from DVD players and VCR player/recorders to ac adapters for cellular phones and digital cameras. An auto-burst standby function reduces power consumption at light load, while multiple protections, including the avalanche-energy guaranteed MOSFET, provide high reliability of system design.

Cycle-by-cycle current limiting, undervoltage lockout with hysteresis, overvoltage protection, and thermal shutdown protect the power supply during the normal overload and fault conditions. Overvoltage protection and thermal shutdown are latched after a short delay. The latch may be reset by cycling the input supply. Low start-up current and a low-power standby mode selected from the secondary circuit completes a comprehensive suite of features.

It is provided in an 8-pin mini-DIP plastic package with pin 6 removed. The leadframe plating is pure Sb, and the package complies with RoHS.



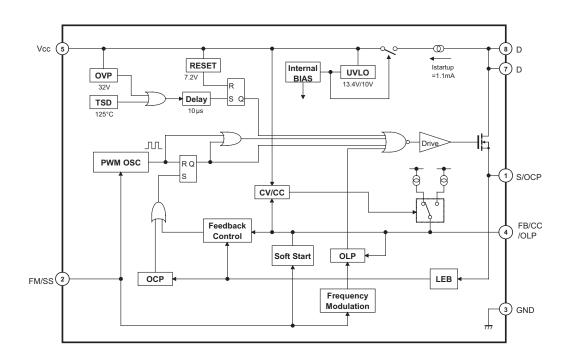
Always order by complete part number, e.g.:

STR-A6259H

All performance characteristics given are typical values for circuit or system baseline design only and are at the nominal operating voltage and an ambient temperature of $+25^{\circ}$ C, unless otherwise stated.



FUNCTIONAL BLOCK DIAGRAM AND TERMINAL ASSIGNMENTS



Number	Name	Description	Functions		
1	S/OCP	Source/OCP terminal	MOSFET Source/Overcurrent protection		
2	FM/SS	FM/Soft start terminal	Capacitor connection terminal for frequency jitter and soft start.		
3	GND	Ground terminal	Ground		
4	FB /CC/OLP	FB/CC/OLP terminal	Input of constant voltage control signal / constant current operation control signal / over load protection signal		
5	VCC	Power supply terminal	Input of power supply for control circuit		
7	D	Drain terminals	MOSFET drain / Input of startup current		
8	U				







ABSOLUTE MAXIMUM RATINGS at $T_A = 25^{\circ}C$

Symbol	Terminal	Note	Max.	Unit
I _{Dpeak}	8-1	Single Pulse	1.8	A
I _{DMAX}	8-1	$V_{S/OCP}$ = 0.83 V with reference to GND, T _A = -20 to 125°C	1.8	Α
	8-1	Single Pulse	24	mJ
LAS		V _{DD} = 99 V, L = 20 mH, I _L = 1.8 A	24	mJ
V _{OCP}	1-3		-0.3 to 6	V
Vcc	5-3		36	V
V _{FB}	4-3		-0.3 to 11	V
V _{FM}	2-3		-0.3 to 6	V
P _{D1}	8-1		1.35	W
P _{D2}	5-3	For Vcc×lcc	0.15	W
T _F		Refer to T _{OP}	-20 to 125	°C
T _{op}			-20 to 125	°C
T _{stg}			-40 to 125	°C
TJ			150	°C
	IDpeak IDMAX EAS VOCP VCC VFB VFM PD1 PD2 TF Top Tstg	IDpeak 8-1 IDmax 8-1 EAS 8-1 VOCP 1-3 VCC 5-3 VFB 4-3 VFM 2-3 PD1 8-1 PD2 5-3 TF Top Tstg	$\begin{tabular}{ c c c c c } \hline I_{Dpeak} & 8-1 & Single Pulse \\ \hline I_{DMAX} & 8-1 & V_{S/OCP} = 0.83 V with reference to GND, \\ \hline T_A = -20 to 125°C \\ \hline E_{AS} & 8-1 & Single Pulse \\ \hline V_{DD} = 99 V, L = 20 mH, I_L = 1.8 A \\ \hline V_{OCP} & 1-3 & V_{CC} & 5-3 \\ \hline V_{FB} & 4-3 & V_{FB} & 4-3 \\ \hline V_{FM} & 2-3 & V_{FM} & 2-3 \\ \hline P_{D1} & 8-1 & P_{D2} & 5-3 & For Vcc×lcc \\ \hline T_F & Refer to T_{OP} \\ \hline T_{op} & & \\ \hline T_{stg} & & \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c c c } \hline I_{Dpeak} & 8-1 & Single Pulse & 1.8 \\ \hline I_{DMAX} & 8-1 & V_{S/OCP} = 0.83 V with reference to GND, $$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$

¹Refer to figure 1

²Refer to figure 3

³Refer to figure 5

⁴Mounted on 15 x 15 mm printed circuit board ⁵Refer to figure 6

⁶Measured at the root of terminal 3

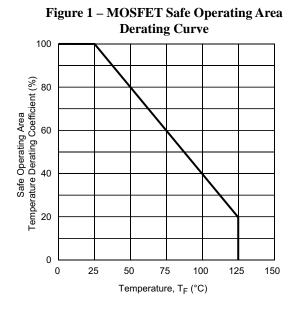
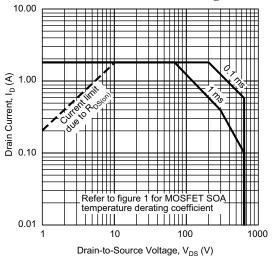


Figure 2 – MOSFET Safe Operating Area Drain Current versus Voltage







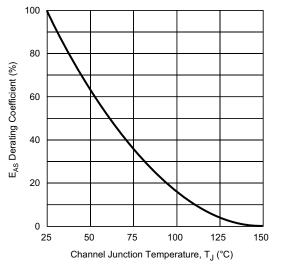
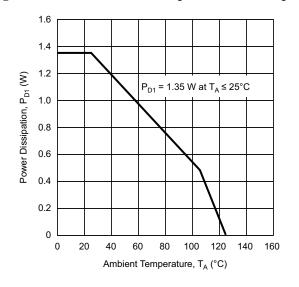
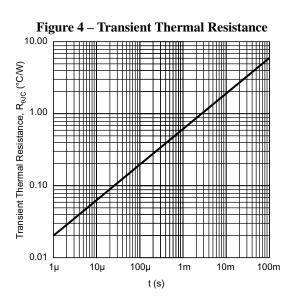


Figure 3 – MOSFET Avalanche Energy Derating Curve

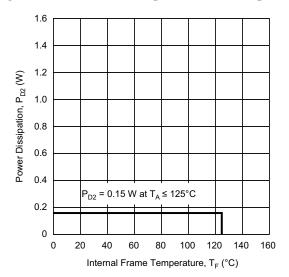
Figure 5 – MOSFET Power Dissipation versus Temperature





Switching Regulators

Figure 6 – MIC Power Dissipation versus Temperature









ELECTRICAL CHARACTERISTICS for Controller (MIC), valid at $T_A = 25^{\circ}$ C, $V_{CC} = 18$ V, unless otherwise specified

Characteristic	Symbol	Terminal	Test Conditions	Min.	Тур.	Max.	Unit
Operation Start Voltage V _{CC(ON)}		5-3	(Power supply voltage at which device starts operating) Measurement circuit 1, V_{CC} = 0 though 13.1 to 16.1 V	12.9	14.3	15.7	V
Operation Stop Voltage V _{CC(OFF)}		5-3	(Power supply voltage at which device stops operating) Measurement circuit 1, V_{CC} = 16.1 through 9 to 11 V	9	10	11	V
Circuit Current In Operation	I _{CC(ON)}	5-3	(Inflow current into power supply terminal, in operation) Measurement circuit 1	-	-	4	mA
Initialization Circuit Current I _{CC(OFF)}		5-3	(Inflow current into power supply terminal, while subject to UVLO prior to operation) Measurement circuit 1, V_{CC} = 13 V	-	-	25	μA
Center Switching Frequency f _{osc(av)}		8-3	(Center oscillation frequency of D terminal) Measurement circuit 2	90	100	110	KHz
Frequency Jitter Deviation Δf		8-3	Maximum frequency – minimum frequency Measurement circuit 2	6	10	14	kHz
Maximum Duty Cycle D _{MAX}		8-3	(Maximum width of the low portion of the D terminal waveform) Measurement circuit 2	70	76	82	%
FM High Voltage	V _{HFM}	2-3	$(V_{FM}$ at which the FM current is changed from 10 μA to $-10~\mu A)$ Measurement circuit 2	4.0	4.5	5.0	v
FM Low Voltage	V_{LFM}	2-3	$(V_{FM}$ at which the FM current is changed from –10 μA to 10 $\mu A)$ Measurement circuit 2	3.2	3.6	4.0	v
FM Outflow Current	I _{sorcFM}	2-3	Outflow current from FM terminal at $V_{FM} = V_{LFM} (3.7 V typ.)$ Measurement circuit 2	7.7	11	15.4	μA
FM Inflow Current	I _{sinkFM}	2-3	Inflow current into FM terminal at $V_{FM} = V_{HFM} (4.4 \text{ V typ.})$ Measurement circuit 2	-15.4	-11	-7.7	μA
OCP Threshold Voltage	$V_{OCP(th)}$	1-3	(The drain current at which the low portion of the D terminal waveform becomes shorter than the high portion, with V_{OCP} increasing) Measurement circuit 3	0.67	0.74	0.81	v
Leading Edge Blanking Time	t _{wb}	8-3	(The low portion of the D terminal waveform with V _{OCP} = 1 V) Measurement circuit 3	240	350	460	ns
Burst Threshold Voltage	$V_{burst(th)}$	4-3	(FB/CC/OLP terminal voltage at which D terminal waveform oscillation stops due to V_{FB} decreasing from 5 V) Measurement circuit 4	1.0	1.12	1.24	v
OLP Threshold Voltage	V _{OLP(th)}	4-3	(FB/CC/OLP terminal voltage at which D terminal waveform oscillation stops due to V_{FB} increasing from 5 V) Measurement circuit 4	7.3	8.6	9.9	v
Output Current at OLP Operation	I _{OLP}	4-3	(Outflow current from FB/CC/OLP terminal at V _{FB} = 8 V) Measurement circuit 4	12	18	25	μA
OLP Delay Time	T _{OLP}	4-3	(Time between surpassing $V_{\text{OLP}(th)}$ and stop of oscillation) Measurement circuit 4	0.84	1.2	1.56	s
Maximum Feedback Current	I _{FB(MAX)}	4-3	(Outflow current from FB/CC/OLP terminal at V _{FB} = 0 V) Measurement circuit 4	220	310	430	μA

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ELECTRICAL CHARACTERISTICS for Controller (MIC) continued, valid at $T_A = 25^{\circ}C$, $V_{CC} = 18$ V, unless otherwise specified

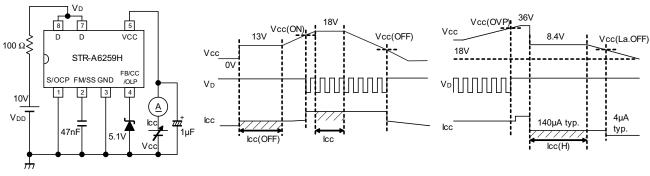
V _{SET(CC)}	4-3	(FB/CC/OLP terminal voltage at which I _{FB} changes from 280 μ A to 16 μ A due to V _{FB} increasing from 5 V) Measurement circuit 4	4.9	5.8	6.7	v
V _{RES(CC)}	4-3	(FB/CC/OLP terminal voltage at which I _{FB} changes from 16 μ A to 280 μ A due to V _{FB} decreasing from 8 V) V _{RES(CC)} × V _{CC} = 25 V, Measurement circuit 4, V _{CC} = 25 V	3.5	3.9	4.3	v
I _{startup}	6-3	(Outflow current from V_{CC} terminal at V_{DD} = 600 V) Measurement circuit 5, V_{CC} = 13 V	0.77	1.1	1.43	mA
V _{CC(OVPth)}	5-3	$(V_{CC} \text{ at which the oscillation of the D terminal waveform stops due to V_{CC} increasing from 18 V) Measurement circuit 1, V_{CC} = 18 through 31 to 35.2 V$	28.8	32	35.2	V
I _{CC(H)}	5-3	(Inflow current into VCC at V_{CC} = 8.4 V, after OVP operation) Measurement circuit 1, V_{CC} = 35.2 to 8.6 V	-	-	270	μA
$V_{CC(LaOFF)}$	5-3	$(V_{CC}$ at which I _{CC} drops below 20 μ A due to decreasing V _{CC} after OVP operation) Measurement circuit 1, V _{CC} = 35.2 through 5.9 to 8.6 V	5.9	7.2	8.6	V
T _{J(TSD)}			125	140	_	°C
TICS for MOSFI	ET, valid at	$T_A = 25^{\circ}C$, $V_{CC} = 18$ V, unless otherwise specified			•	
V _{DSS}	8-1	Measurement circuit 6, I_D = 300 μ A	650	Ι	-	V
I _{DSS}	8-1	(Inflow current into D terminal at V _{DD} = 650 V) Measurement circuit 5	-	-	300	μA
R _{DS(ON)}	8-1	Measurement circuit 3, I _D = 0.4 A	-	-	6	Ω
t _f	8-1	Measurement circuit 2	-	-	250	ns
$R_{\theta ch-F}$		Between channel and internal frame; measured at the root of terminal 3	_	-	52	°C/W
	V _{RES} (CC) I _{startup} V _{CC} (OVPth) I _{CC} (H) V _{CC} (LaOFF) TJ(TSD) TICS for MOSFI V _{DSS} I _{DSS} R _{DS} (ON) t _f	VRES(CC) 4-3 Istartup 6-3 V _{CC(OVPth)} 5-3 I _{CC(H)} 5-3 V _{CC(LaOFF)} 5-3 T _{J(TSD)} TICS for MOSFET, valid at V _{DSS} 8-1 I _{DSS} 8-1 R _{DS(ON)} 8-1 t _r 8-1	$ \begin{array}{c c} V_{\text{SET(CC)}} & 4-3 & 280 \ \mu\text{A to } 16 \ \mu\text{A due to } V_{\text{Fe}} \ \text{increasing from 5 V}) \\ \hline \text{Measurement circuit 4} \\ \hline V_{\text{RES(CC)}} & 4-3 & (FB/CC/OLP \ \text{terminal voltage at which } I_{\text{FB}} \ \text{changes from } 8 \ \text{V}) \\ \hline V_{\text{RES(CC)}} \times V_{\text{CC}} = 25 \ \text{V}, \ \text{Measurement circuit 4}, \ V_{\text{CC}} = 25 \ \text{V} \\ \hline I_{\text{startup}} & 6-3 & (Outflow \ \text{current from } V_{\text{CC}} \ \text{terminal at } V_{\text{DD}} = 600 \ \text{V}) \\ \hline \text{Measurement circuit 5}, \ V_{\text{CC}} = 13 \ \text{V} \\ \hline V_{\text{CC(OVPth)}} & 5-3 & (V_{\text{CC}} \ \text{at which the oscillation of the D terminal waveform stops due to } V_{\text{CC}} \ \text{inflow current into } V_{\text{CC}} \ \text{at which } 18 \ \text{V} \\ \hline M_{\text{easurement circuit 1}, \ V_{\text{CC}} = 18 \ \text{through 31 to } 35.2 \ \text{V} \\ \hline I_{\text{CC(H)}} & 5-3 & (Inflow \ \text{current into } VCC \ \text{at } V_{\text{CC}} \ \text{at which } 1_{\text{CC}} \ \text{operation} \\ \hline M_{\text{easurement circuit 1}, \ V_{\text{CC}} \ \text{at } 8.4 \ \text{V}, \ \text{after OVP} \\ \hline \text{operation} \\ \hline M_{\text{easurement circuit 1}, \ V_{\text{CC}} \ \text{at } 8.4 \ \text{V}, \ \text{after OVP} \\ \hline \text{operation} \\ \hline M_{\text{easurement circuit 1}, \ V_{\text{CC}} \ \text{at } 3.2 \ \text{through 31 to } 35.2 \ \text{V} \\ \hline \hline V_{\text{CC(LaOFF)}} & 5-3 & (V_{\text{CC}} \ \text{at which } I_{\text{CC}} \ \text{drops below } 20 \ \mu\text{A due to decreasing} \\ \hline V_{\text{CC}} \ \text{at } \text{operation} \\ \hline M_{\text{easurement circuit 1}, \ V_{\text{CC}} \ \text{at } 3.2 \ \text{through 5.9 to } 8.6 \ \text{V} \\ \hline \hline T_{\text{J}(\text{TSD}} \\ \hline \hline \text{TICS for MOSFET, valid at } T_{\text{A}} \ \text{a } 25^{\circ}\text{C}, \ V_{\text{CC}} \ \text{a } 18 \ \text{V}, \ \text{unless otherwise specified} \\ \hline \hline V_{\text{DSS}} \ \ \ensurement circuit 5, \ I_{\text{D}} \ \text{a } 300 \ \mu\text{A} \\ \hline I_{\text{DSS}} \ \ \ensurement circuit 5, \ I_{\text{D}} \ \text{a } 300 \ \mu\text{A} \\ \hline \hline M_{\text{easurement circuit 5}, \ I_{\text{D}} \ \text{a } 300 \ \mu\text{A} \\ \hline \hline I_{\text{DSS}} \ \ \ensurement circuit 2 \\ \hline \hline R_{\text{DS(ON)}} \ \ \ensurement circuit 2 \\ \hline \hline \text{Between channel and internal frame; measured at the root} \\ \hline \ensurement circuit 2 \\ \hline \ensurement circuit 2$	$V_{SET(CC)}$ 4-3280 µA to 16 µA due to V_{FB} increasing from 5 V) Measurement circuit 44.9 $V_{RES(CC)}$ 4-3(FB/CC/OLP terminal voltage at which I_{FB} changes from 16 µA to 280 µA due to V_{FB} decreasing from 8 V) $V_{RES(CC)} \times V_{CC} = 25 V$, Measurement circuit 4, $V_{CC} = 25 V$ 3.5 $I_{startup}$ 6-3(Outflow current from V_{CC} terminal at $V_{DD} = 600 V$) Measurement circuit 5, $V_{CC} = 13 V$ 0.77 $V_{CC(0VPth)}$ 5-3(V_{CC} at which the oscillation of the D terminal waveform stops due to V_{CC} increasing from 18 V) Measurement circuit 1, $V_{CC} = 18$ through 31 to 35.2 V28.8 $I_{CC(H)}$ 5-3(Inflow current into VCC at $V_{CC} = 8.4 V$, after OVP operation) Measurement circuit 1, $V_{CC} = 35.2$ to 8.6 V- $V_{CC(LaOFF)}$ 5-3(V_{C c at which I_{CC} drops below 20 µA due to decreasing V_{CC} after OVP operation) Measurement circuit 1, $V_{CC} = 35.2$ through 5.9 to 8.6 V5.9TICS for MOSFET, valid at $T_A = 25^{\circ}$ C, $V_{CC} = 18 V$, unless otherwise specified125 V_{DSS} 8-1Measurement circuit 6, $I_D = 300 \mu A$ 650 I_{DSS} 8-1(Inflow current into D terminal at $V_{DD} = 650 V$) measurement circuit 5- $R_{DS(ON)}$ 8-1Measurement circuit 2- $R_{OL,N}$ 8-1Measurement circuit 2- $R_{OL,N}$ 8-1Measurement circuit 4-	$V_{SET(CC)}$ 4-3280 μ A to 16 μ A due to V_{FB} increasing from 5 V) Measurement circuit 44.95.8 $V_{RES(CC)}$ 4-3(FB/CC/OLP terminal voltage at which IFB changes from 16 μ A to 280 μ A due to V_{FB} decreasing from 8 V) $V_{RES(CC)} \times V_{CC} = 25 V$ 3.53.9 $I_{startup}$ 6-3(Outflow current from V_{CC} terminal at $V_{DD} = 600 V$) Measurement circuit 5, $V_{CC} = 13 V$ 0.771.1 $V_{CC(OVPth)}$ 5-3(V_{CC} at which the oscillation of the D terminal waveform stops due to V_{CC} increasing from 18 V) Measurement circuit 1, $V_{CC} = 18$ through 31 to 35.2 V28.832 $I_{CC(H)}$ 5-3(Inflow current into VCC at $V_{CC} = 8.4 V$, after OVP operation) Measurement circuit 1, $V_{CC} = 35.2$ to 8.6 V $V_{CC(LaOFF)}$ 5-3(V_{Cc} at which Icc drops below 20 μ A due to decreasing V_{CC} after OVP operation) Measurement circuit 1, $V_{CC} = 35.2$ through 5.9 to 8.6 V7.2 $T_{J(TSD)}$ 125140TICS for MOSFET, valid at $T_A = 25^{\circ}$ C, $V_{CC} = 18 V$, unless otherwise specified- V_{DSS} 8-1Measurement circuit 5, $I_D = 300 \mu$ A650 I_{DSS} 8-1Measurement circuit 5, $I_D = 650 V$) Measurement circuit 5- $R_{DS(ON)}$ 8-1Measurement circuit 2- $R_{DS(O$	$V_{SET(CC)}$ 4-3280 µA to 16 µA due to V_{FB} increasing from 5 V) Measurement circuit 44.95.86.7 $V_{RES(CC)}$ 4-3(FB/CC/OLP terminal voltage at which I_{FB} changes from 16 µA to 280 µA due to V_{FB} decreasing from 8 V) $V_{RES(CC)} \times V_{CC} = 25 V$, Measurement circuit 4, $V_{CC} = 25 V$ 3.53.94.3 $I_{startup}$ 6-3(Outflow current from V_{CC} terminal at $V_{DD} = 600 V$) Measurement circuit 5, $V_{CC} = 13 V$ 0.771.11.43 $V_{CC(0VPth)}$ 5-3(Outflow current from V_{CC} terminal at $V_{DD} = 600 V$) Measurement circuit 1, $V_{CC} = 13 V$ 28.83235.2 $I_{CC(H)}$ 5-3(McC at which the oscillation of the D terminal waveform stops due to V_{CC} increasing from 18 V) Measurement circuit 1, $V_{CC} = 18$ through 31 to 35.2 V28.83235.2 $I_{CC(H)}$ 5-3(Inflow current into VCC at $V_{CC} = 35.2$ to 8.6 V270 $V_{CC(LaOFF)}$ 5-3(V_{Cc} at which I_{Cc} drops below 20 µA due to decreasing V_{Cc} after OVP operation) Measurement circuit 1, $V_{CC} = 35.2$ through 5.9 to 8.6 V5.97.28.6 $T_{J(TSD)}$ Image: the summer tricuit 1, $V_{CC} = 35.2$ through 5.9 to 8.6 V125140-TICS for MOSFET, valid at T_A = 25°C, $V_{CC} = 18 V$, unless otherwise specified300 V_{DSS} 8-1Measurement circuit 6, $I_D = 300 \mu A$ 650 I_{DS} 8-1Measurement circuit 3, $I_D = 0.4 A$ 6 I_V 8-1<

¹Latch circuit enabled when OVP and TSD in operation



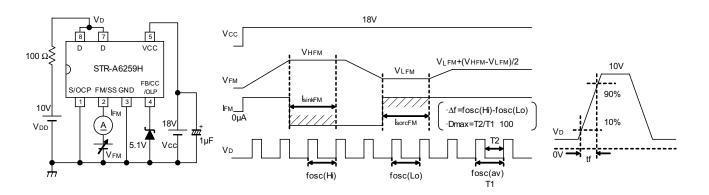




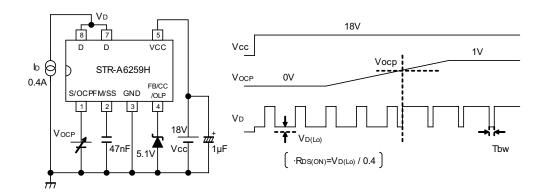


Measurement Circuit 1

Measurement Circuit 2



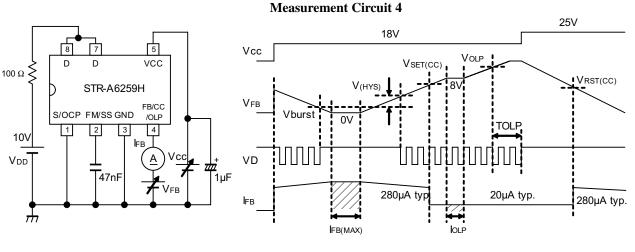
Measurement Circuit 3



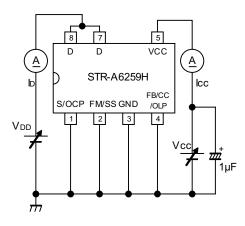




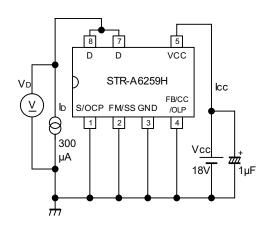




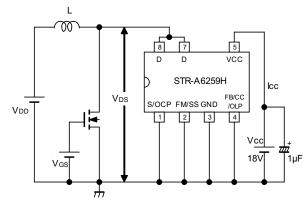
Measurement Circuit 5



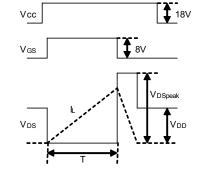
Measurement Circuit 6



Measurement Circuit 7



$$E_{AS} = \frac{L}{2} \left(I_{Lpeak} \right)^2 \times \frac{V_{DSpeak}}{V_{DSpeak} - V_{DD}}$$



Adjust T such that $I_{Lpeak} = 1.8 \text{ A}$

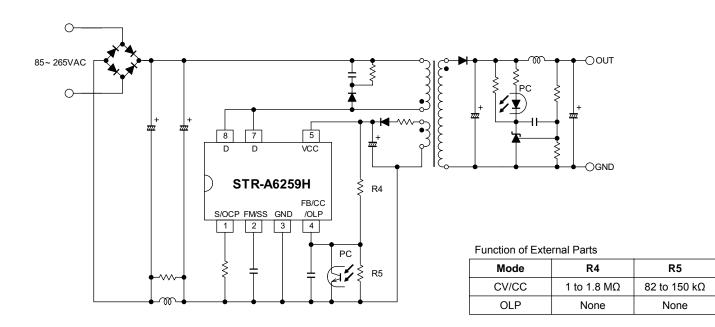






TYPICAL APPLICATION CIRCUIT

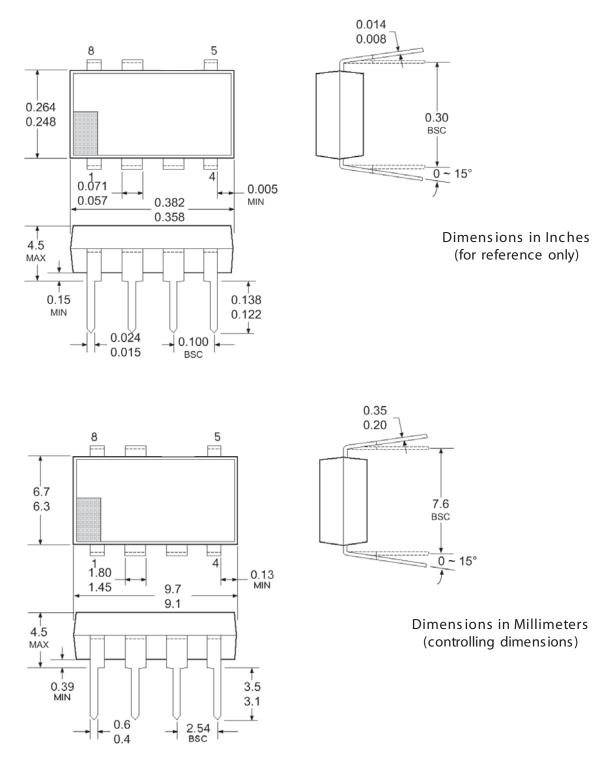
For improved thermal dissipation, connect terminals 7 and 8 to as large an area of exposed copper as possible









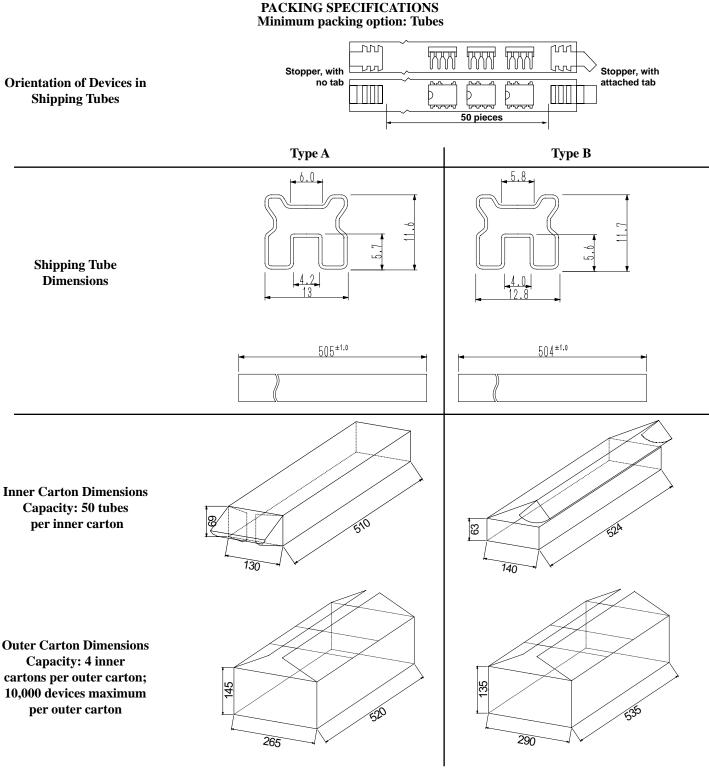


PACKAGE DIMENSIONS, DIP-8









All dimensions: mm







WARNING — These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

The use of an isolation transformer is recommended during circuit development and breadboarding.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions. Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40 to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- · Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- · Reinspect for rust in leads and solderability of products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between adjacent products, and shorts to the heatsink.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting this product on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce stress.
- Volatile-type silicone greases may produce cracks after long periods of time, resulting in reduced heat radiation effect. Silicone grease with low consistency (hard grease) may cause cracks in the mold resin when screwing the product to a heatsink.
- Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Туре	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	Toshiba Silicone Co., Ltd.
SC102	Dow Corning Toray Silicone Co., Ltd.

Soldering

When soldering the products, please be sure to minimize the working time, within the following limits:

260±5°C 10 s

350±5°C 3 s

· Soldering iron should be at a distance of at least 1.5 mm from the body of the products

Electrostatic Discharge

- When handling the products, operator must be grounded. Grounded wrist straps worn should have at least 1 MΩ of resistance to ground to prevent shock hazard.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- · When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in other to prevent leak voltages generated by them
 from being applied to the products.
- · The products should always be stored and transported in our shipping containers or conductive containers, or be wrapped in aluminum foil.





When using the products described herein, the applicability and suitability of such products for the intended purpose shall be reviewed at the users responsibility.

Although Sanken undertakes to enhance the quality and reliability of its products, the occurrence of failure and defect of semiconductor products at a certain rate is inevitable.

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115 Northeast Cutoff, Box 15036 Worcester, Massachusetts 01615-0036



Switching Reductors

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